



最大収容 **701**席

							A	12	13	14	15	16	17	18	19	20	21	22	A												
						8	9	10	11	B	12	13	14	15	16	17	18	19	20	21	22	B	23	24	25	26					
					7	8	9	10	11	C	12	13	14	15	16	17	18	19	20	21	22	C	23	24	25	26	27				
				6	7	8	9	10	11	D	12	13	14	15	16	17	18	19	20	21	22	D	23	24	25	26	27	28			
			5	6	7	8	9	10	11	E	12	13	14	15	16	17	18	19	20	21	22	E	23	24	25	26	27	28	29		
4	5	6	7	8	9	10	11	F	12	13	14	15	16	17	18	19	20	21	22	F	23	24	25	26	27	28	29	30			

2 階席

The diagram illustrates the construction of a 32-bit word from four 11-bit registers (R, S, T, U) and four 5-bit registers (V, W, X, Y). The 11-bit registers are shown as a 4x11 grid of cells, each containing a number from 1 to 11. The 5-bit registers are shown as a 4x5 grid of cells, each containing a number from 1 to 5. The registers are labeled R, S, T, U, V, W, X, and Y. The registers R, S, T, and U are connected to the registers V, W, X, and Y via a series of lines, indicating a data flow or mapping. The registers V, W, X, and Y are shown as a 4x5 grid of cells, each containing a number from 1 to 5. The registers are labeled V, W, X, and Y. The registers V, W, X, and Y are connected to the registers R, S, T, and U via a series of lines, indicating a data flow or mapping.

